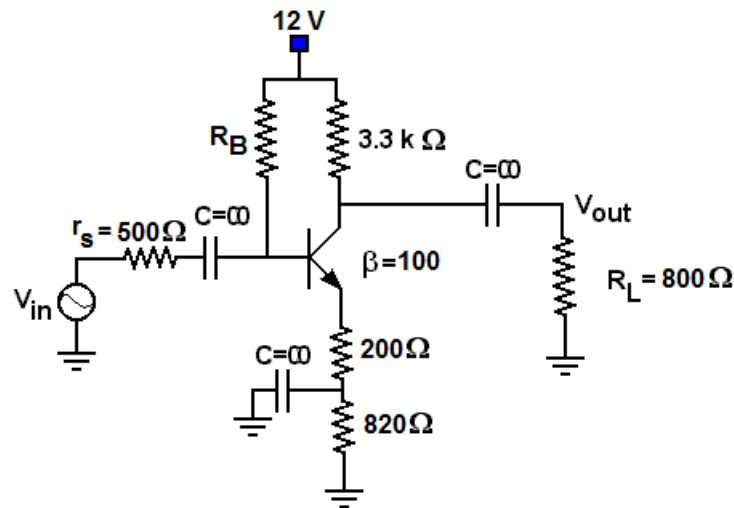


Electronics

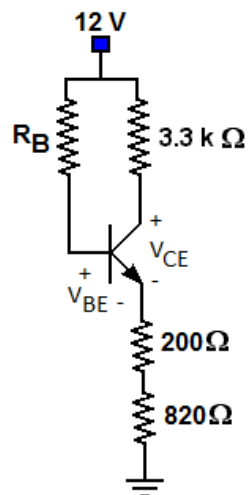
More examples with BJTs

Problem 1.- Considering the circuit shown below,

- Calculate the value of R_B to bias the transistor in the middle of the active region.
- Calculate the current, voltage and power of the transistor at the operating point.
- Calculate the input impedance.
- Calculate the output impedance.
- Calculate the voltage gain with and without load.
- Calculate the gain in power.



Solution: First, we do the DC analysis with the capacitors modeled as open circuits.



In the collector-emitter branch, we can write this equation

$$12V = 3.3k\Omega I_C + V_{CE} + (200\Omega + 820\Omega)I_E$$

Knowing that $I_C = \beta I_B$ and $I_E = (\beta + 1)I_B$, we replace these variables obtaining

$$12V = 3.3k\Omega\beta + V_{CE} + (200\Omega + 820\Omega)(\beta + 1)I_B$$

If we want the transistor in the middle of the active region, V_{CE} has to be 6V. Hence, we can calculate the base current using the equation above

$$I_B = \frac{12V - 6V}{3.3k\Omega\beta + (200\Omega + 820\Omega)(\beta + 1)} = \frac{12V - 6V}{3.3k\Omega \times 100 + (200\Omega + 820\Omega)(100 + 1)} = 13.9\mu A$$

Also the collector and emitter currents

$$I_C = \beta I_B = 100 \times 13.9\mu A = 1.39mA$$

$$I_E = (\beta + 1)I_B = 101 \times 13.9\mu A = 1.40mA$$

To find the base resistor consider the circuit branch on the base side:

$$12V = R_B I_B + V_{BE} + (200\Omega + 820\Omega)I_E$$

The voltage V_{BE} can be approximated to 0.7V and with the calculated values of the currents, we find:

$$R_B = \frac{12V - 0.7V - (200\Omega + 820\Omega)I_E}{I_B} = \frac{12V - 0.7V - (200\Omega + 820\Omega)1.4mA}{13.9\mu A} = 712k\Omega$$

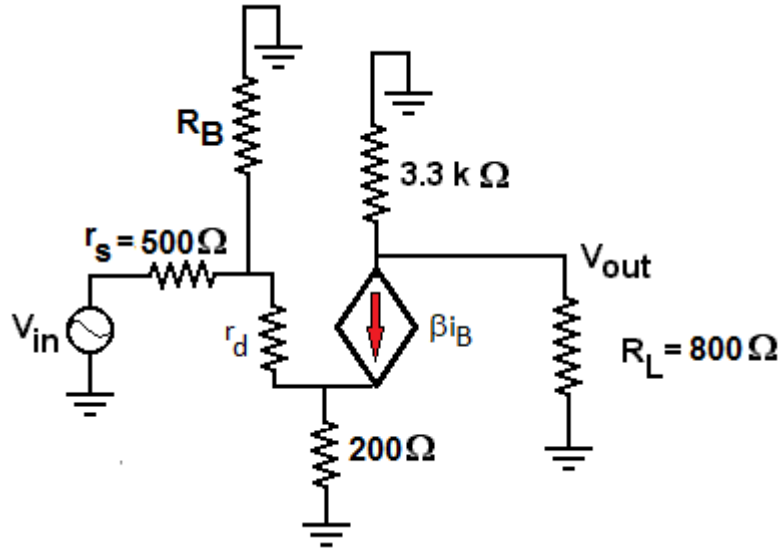
The power dissipated by the transistor is

$$P = V_{CE}I_C + V_{BE}I_B = 6V \times 1.39mA + 0.7V \times 13.9\mu A = 8.32mW$$

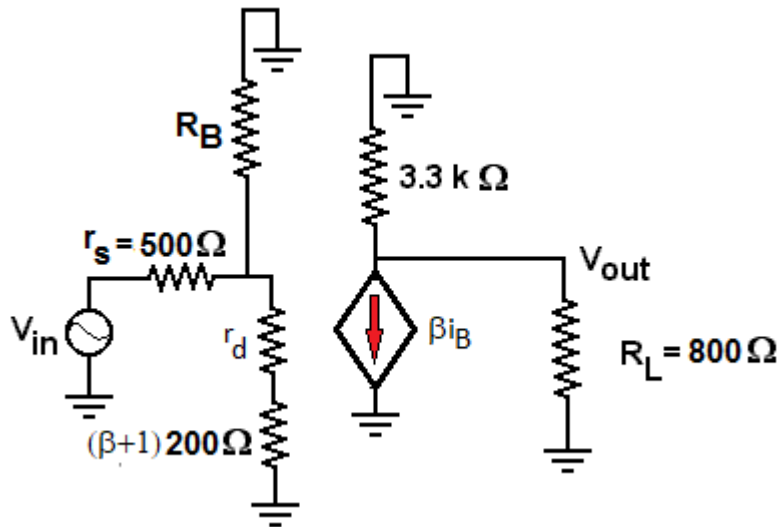
For the AC analysis, the base-emitter junction will be modeled as a dynamic resistance with value

$$r_d = \frac{26mV}{I_B} = \frac{26mV}{13.9\mu A} = 1.88k\Omega$$

From the point of view of the collector, the transistor behaves as a dependent current source. This gives us the following AC model.



Because the capacitors behave as short-circuits in AC, the 820Ω emitter resistance doesn't participate in the analysis, but the 200Ω does. The current that runs through this resistance is $(\beta + 1)$ times the base current, so it can be modeled as a $200\Omega \times (\beta + 1)$ resistance from the point of view of the base.



The input impedance of the amplifier is

$$Z_i = R_B \parallel [r_d + 200\Omega \times (\beta + 1)] = 712k\Omega \parallel [1.88k\Omega + 200\Omega \times 101] = 21.4k\Omega$$

The output impedance is the 3.3k Ohm resistance

$$Z_o = 3.3k\Omega$$

To find the voltage gain we calculate the base current in AC. To do this, we find the Thevenin equivalent of the signal source, its resistance and the base resistance:

$$V_{Th} = V_{in} \frac{712k\Omega}{712k\Omega + 500\Omega} = 0.999V_{in} \quad R_{Th} = \frac{712k\Omega \times 500\Omega}{712k\Omega + 500\Omega} = 500\Omega$$

The base current in AC is

$$i_B = \frac{0.999V_{in}}{500\Omega + 1.88k\Omega + 200\Omega(100 + 1)}$$

The collector current in AC is

$$i_C = \beta i_B = \frac{100 \times 0.999V_{in}}{500\Omega + 1.88k\Omega + 200\Omega(100 + 1)}$$

Without load, this current will run through the $3.3k\Omega$ producing an output voltage of

$$V_{out} = -3.3k\Omega i_C = -\frac{100 \times 0.999 \times 3.3k\Omega}{500\Omega + 1.88k\Omega + 200\Omega(100 + 1)} V_{in} = -14.6V_{in}$$

That is a gain of -14.6

If we connect the load, the collector current will run through the combination of resistances in parallel, that is

$$R = 3.3k\Omega // 800\Omega = \frac{800\Omega \times 3.3k\Omega}{800\Omega + 3.3k\Omega} = 644\Omega$$

And the output voltage will be

$$V_{out} = -644\Omega i_C = -\frac{100 \times 0.999 \times 644\Omega}{500\Omega + 1.88k\Omega + 200\Omega(100 + 1)} V_{in} = -2.85V_{in}$$

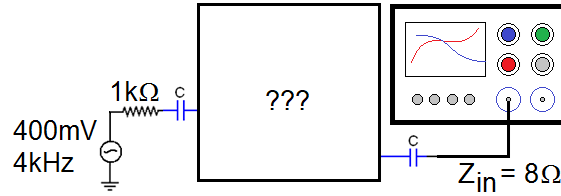
This is a gain of -2.85

The power gain without load is zero, and with load it is:

$$\frac{P_{out}}{P_{in}} = \left(\frac{V_{out}}{V_{in}} \right)^2 \frac{Z_{in}}{Z_L} = (-2.85)^2 \frac{21.4k\Omega}{800\Omega} = 217$$

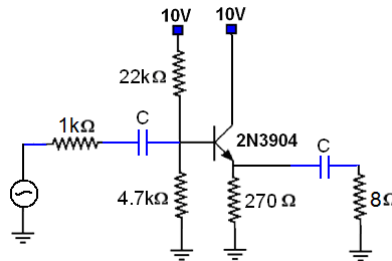
In decibels, this is $10 \log(217) = 23.4dB$

Problem 2.- You want to connect an instrument with low input impedance (8 ohm) with an analog audio signal source (~4kHz) of 400mV amplitude that has an output impedance of 1kohm. The voltage gain is not crucial (it can be ~1), but it is important to couple the impedances.



Propose a circuit based on transistors to accomplish this and calculate
 (a) The voltage gain
 (b) The input impedance of the amplifier

Solution.- We could use an emitter follower to approximate the impedances. Consider, for example the following circuit that you can build in the lab and with $\beta = 100$.



In DC the capacitors behave as open circuits and the voltage divider that feeds the base gives us the following Thevenin equivalent

$$V_{th} = 10V \frac{4.7k\Omega}{4.7k\Omega + 22k\Omega} = 1.76V$$

$$R_{th} = \frac{4.7k\Omega \times 22k\Omega}{4.7k\Omega + 22k\Omega} = 3.87k\Omega$$

The base current will be

$$I_{BQ} = \frac{1.76V - 0.7V}{3.87k\Omega + 101 \times 0.27k\Omega} = 34.0\mu A$$

The Q point is

$$I_{CQ} = \beta I_{BQ} = 100 \times 34.0\mu A = 3.4mA$$

$$V_{CE} = 10V - 3.4mA \times 270\Omega = 9.08V$$

The AC dynamic resistance is:

$$r_d = \frac{26mV}{34\mu A} = 765\Omega$$

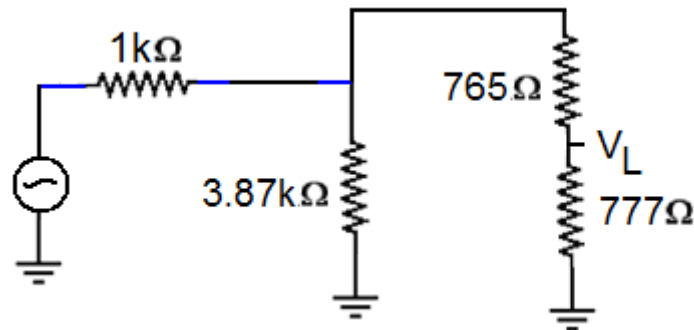
The load impedance in parallel with the emitter resistance give us

$$R_E \parallel R_L = \frac{8\Omega \times 270\Omega}{270\Omega + 8\Omega} = 7.77\Omega$$

Reflected to the base it is

$$\beta(R_E \parallel R_L) = 777\Omega$$

The AC model is



The input signal has a Thevenin equivalent

$$v_{th} = v_{in} \frac{3.87k\Omega}{1k\Omega + 3.87k\Omega} = 0.795v_{in}$$

$$r_{th} = \frac{3.87k\Omega \times 1k\Omega}{1k\Omega + 3.87k\Omega} = 0.795k\Omega$$

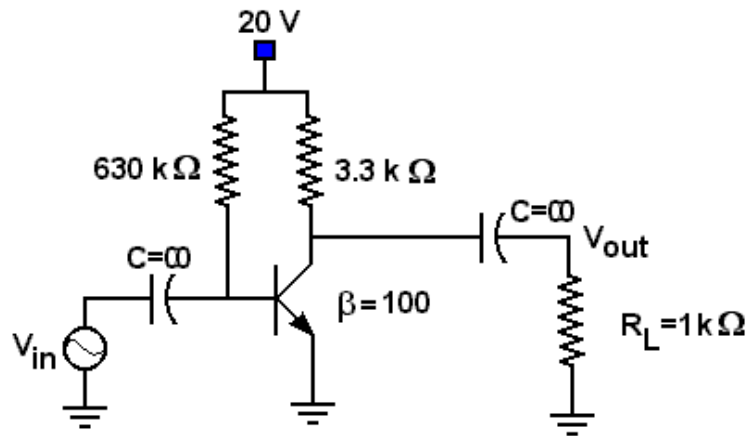
So, the voltage gain is:

$$A = 0.795v_{in} \frac{777\Omega}{777\Omega + 765\Omega + 795\Omega} = 0.264v_{in}$$

And the input impedance

$$Z_{in} = 1k\Omega + 3.87k\Omega \parallel (765\Omega + 777\Omega) = 2.1k\Omega$$

Problem 3.- The following circuit is a typical amplifier based on one transistor. It is a simple circuit, but it has the drawback that the strong dependence of the Q point on beta. Calculate the Q point (V_{CE} , I_{CQ}) and the voltage gain for beta 50 and 150.



Solution.- This is a direct polarization with a fixed DC base current:

$$I_{BQ} = \frac{20 - 0.7}{630k} = 30.6\mu A$$

The dynamic resistance is $r_d = \frac{26mV}{30.6\mu A} = 849\Omega$

The collector current and collector-emitter voltage depend on beta:

$$\beta = 50 \rightarrow I_{CQ} = 50 \times 30.6\mu A = 1.53mA \rightarrow V_{CE} = 20 - 1.53mA \times 3.3k\Omega = 14.9V$$

$$\beta = 150 \rightarrow I_{CQ} = 150 \times 30.6\mu A = 4.59mA \rightarrow V_{CE} = 20 - 4.59mA \times 3.3k\Omega = 4.83V$$

The two voltage gains are:

$$\beta = 50 \rightarrow A = -50 \times \frac{1k // 3.3k}{849\Omega} = -45.2$$

$$\beta = 150 \rightarrow A = -150 \times \frac{1k // 3.3k}{849\Omega} = -135.6$$